

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Thomas Rueckes et al. Confirmation No.: 2813
Application No.: 10/811,373 Art Unit: 2826
Filed: March 26, 2004 Examiner: E. A. Rodela
Title: NANOTUBE-ON-GATE FET STRUCTURES AND APPLICATIONS

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION PURSUANT TO 37 CFR 1.322

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted a typographical error which should be corrected.

In the Claims:

In Claim 8, line 13, should read 4F² instead of 4F₂.

The error was not in the application as filed by applicant; accordingly no fee is required.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Applicant believes no fee is due with this request. However, if a fee is due, please charge our Deposit Account No. 08-0219, under Order No. 0112020.00148US2 from which the undersigned is authorized to draw.

Respectfully submitted,

Dated:

12/6/07



Jaihng D. Choi
Registration No.: 59,198
Attorney for Applicant(s)

Wilmer Cutler Pickering Hale and Dorr LLP
60 State Street
Boston, Massachusetts 02109
(617) 526-6000 (telephone)
(617) 526-5000 (facsimile)

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CERTIFICATE OF CORRECTIONPage 1 of 1

PATENT NO. : 7,294,877
APPLICATION NO. : 10/811,373
ISSUE DATE : November 13, 2007
INVENTOR(S) : Thomas RUECKES et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

** Claim 8, line 13 should read "the device has an area of about 4F²."**

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Jaime D. Choi
WILMER CUTLER PICKERING HAILE AND DORR LLP
60 State Street
Boston, Massachusetts 02109

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